IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Alan Courtay

Assignee: Synopsys, Inc.

Title: Mixed-Signal Finite State Machine Models Ensuring Analog Continuity

Serial No.: 10/683,925 File Date: October 10, 2003

Examiner: Russell L. Guill Art Unit: 2123

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Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT IN RESPONSE TO THE FIRST OFFICE ACTION

Initial Comments:

This response addresses the status of Claims 1-10. Claim 8 is objected to because of an informality. Claims 1-10 are rejected under 35 U.S.C. 101 as being directed to non-statutory subject matter. Claims 1-2 and 4-10 are rejected under 35 U.S.C. 103(a) as being rendered obvious by "Special Issue On Hybrid Systems: Theory and Application, A Brief Introduction to the Theory and Applications of Hybrid Systems" (Antsaklis) in view of "VHDL-AMS, A Hardware Description Language For Analog and Mixed Signal Applications" (Christen). Claim 3 is rejected under 35 U.S.C. 103(a) as being rendered obvious by Antsaklis in view of Christen and further in view of "Fuzzy-Logic Digita-Analogue Interfaces For Accurate Mixed Signal Simulation" (Kazmierski). Claims 1 and 8 are amended herein.